



Appl. Ser. No. 10/502,265  
Status Request dated April 4, 2006

TFW 3742  
PATENT  
Atty. Dkt. No. 81919.0020  
Customer No. 26021

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Michitaka KAMEYAMA, et al.

Serial No.: 10/502,265

Filed: July 22, 2004

For: LOGICAL OPERATION CIRCUIT  
AND LOGICAL OPERATION  
METHOD

Art Unit: 3742

Examiner: Not Assigned

Confirmation No.: 9388

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*Sheila Goldner* 04/04/06

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**STATUS REQUEST**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This application was filed on July 22, 2004 and receipt acknowledged by the U.S. Patent and Trademark Office ("USPTO") in the Filing Receipt dated March 21, 2005. The last correspondence from the USPTO was a Notice of Non-Recordation of Document, which we received on February 14, 2006. However, we are still waiting for a first Office Action.

Please send the undersigned a Status Report on this application.

Respectfully submitted,

HOGAN & HARTSON L.L.P.

Date: April 4, 2006

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